

(FILE 'HOME' ENTERED AT 11:18:11 ON 04 JAN 2006)

FILE 'USPATFULL' ENTERED AT 11:18:54 ON 04 JAN 2006

SET HIGH OFF

L1 244 S 712/28/NCL
L2 139 S 712/29/NCL
L3 112 S 712/30/NCL
L4 369 S 712/32/NCL
L5 330 S 712/220/NCL
L6 516 S 712/225/NCL
L7 527 S 712/245/NCL
L8 608 S 713/502/NCL
L9 1391 S 718/100/NCL
L10 1108 S 718/102/NCL
L11 96 S L1-7- NOT AD>20000905
L12 1549 S L1-7 NOT AD>20000905
L13 2618 S 713/400-601/NCL NOT AD>20000905
L14 1257 S (718/100/NCL OR 718/102/NCL) NOT AD>20000905

SET HIGH ON

L15 613264 S (CONTROL? OR SCHEDUL? OR MANAG?) (5A) (PROCESS OR TASK OR THR
L16 51907 S ((CONTROL? OR SCHEDUL? OR MANAG?) (5A) (PROCESS OR TASK OR TH
L17 314728 S INTEGRATED CIRCUIT OR (IC (10A) CHIP)
L18 12608 S L15 (P) L17
L19 94 S L12 AND L18
L20 101 S L13 AND L18
L21 35 S L14 AND L18
L22 260574 S (TRANSFER? OR MOV? OR REASSIGN?) (5A) (COMMAND OR INSTRUCTION
L23 64710 S L15 (P) L22 NOT L19-21
L24 10202 S L16 AND L23
L25 109 S L12 AND L24
L26 111 S L13 AND L24
L27 103 S L14 AND L24
L28 7632 S L17 (P) L22 NOT (L19-21 OR L25-27)
L29 21 S L12 AND L28
L30 80 S L13 AND L28
L31 5 S L14 AND L28
L32 1555 S (CHAIN? OR GROUP?) (5A) (SEQUEN? OR SERIES OR SERIAL? OR SUCC
L33 289 S L15 (P) L32 NOT (L19-21 OR L25-27 OR L29-31)
L34 289 FOCUS L33 1-
L35 10 S L12-14 AND L33
L36 28 S L17 (P) L32 NOT (L19-21 OR L25-27 OR L29-31)
L37 206 S L22 (P) L32 NOT (L19-21 OR L25-27 OR L29-31)
L38 206 FOCUS L37 1-
L39 5 S L12-14 AND L37
L40 228 S L16 AND L32
L41 228 FOCUS L40 1-
L42 23 S L12-14 AND L40

L1 97 S (SCHEDUL? AND TRANSFER AND INTEGRATED CIRCUIT)/CLM
L2 0 S (NOTIF? AND SECOND AND IC COMPONENT)/CLM
L3 265 S (NOTIF? AND SECOND (4W) COMPONENT)/CLM
L4 0 S L1 (L) L3
L5 31 S (NOTIF? AND SECOND AND COMPONENT AND INTEGRATED CIRCUIT)/CLM
L6 165 S (SCHEDUL? AND TRANSFER AND (INTEGRATED OR CHIP) AND (COMPONEN
L7 1700 S (NOTIF? AND SECOND (4W) (UNIT OR PROCESSOR OR COMPONENT OR EL
L8 4 S L6 (L) L7
L9 384 S (SCHEDUL? AND (ASSIGN? OR SEND? OR DESIGNAT? OR TRANSFER? OR
L10 5 S L7 AND L9

(FILE 'HOME' ENTERED AT 11:19:12 ON 12 JAN 2006)

FILE 'USPATFULL' ENTERED AT 11:19:22 ON 12 JAN 2006

L1 97 S (SCHEDUL? AND TRANSFER AND INTEGRATED CIRCUIT)/CLM
L2 0 S (NOTIF? AND SECOND AND IC COMPONENT)/CLM
L3 265 S (NOTIF? AND SECOND (4W) COMPONENT)/CLM
L4 0 S L1 (L) L3
L5 31 S (NOTIF? AND SECOND AND COMPONENT AND INTEGRATED CIRCUIT)/CLM
L6 165 S (SCHEDUL? AND TRANSFER AND (INTEGRATED OR CHIP) AND (COMPONEN
L7 1700 S (NOTIF? AND SECOND (4W) (UNIT OR PROCESSOR OR COMPONENT OR EL
L8 4 S L6 (L) L7
L9 384 S (SCHEDUL? AND (ASSIGN? OR SEND? OR DESIGNAT? OR TRANSFER? OR
L10 5 S L7 AND L9
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